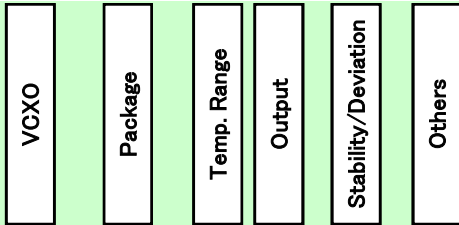


VCXO Part Number Structure

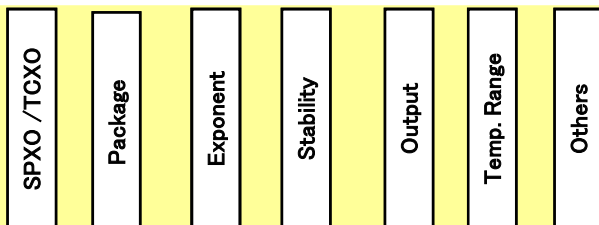
VX SK - 0 1 A S□-□□.□□□□□□MHz
 ① ② ③ ④ ⑤ ⑥ Freq.



①		②		③		④		⑤		
Type		Package		Temp. Range		Output Type (VCXO)		Stability/Deviation		
Code	Type	Code	Size(mm)	Code	deg.C	Code	Output	Code	Stability	Deviation
VX	VCXO	SMT		G	-10~70°C	1	TTL/HCMOS	Vc=0.5~4.5V		
CH	SPXO	SG	14.2X9.6X5.7	J	0~80°C	2	ECL -5.2V	A	±20ppm	±50ppm
CH	TCXO	SG30	14.2X9.6X3.0	0	0~70°C	3	PECL +5V	B	±30ppm	±100ppm
COH	14-pin SPXO/ TCXO	SK	20.0X12.5X5.7	Q	-30~75°C	4	Sinewave	C	±40ppm	±150ppm
		SK04	20.0X12.5X5.7	S	-20~60°C	5	TTL	D	±50ppm	±200ppm
		SW	21.3X18.1X5.7	T	-10~60°C	6	LVCMOS 3.3V			
				V	0~50°C	7	LVPECL 3.3V	Vc=0.15~3.15V		
				W	0~70°C	8	Sinewave 3.3V	G	±30ppm	±60ppm
		DIP		X	-20~70°C			H	±40ppm	±100ppm
		A	14pin	Y	-40~85°C			J	±20ppm	±50ppm
				Z	Others			S□	Others	

SPXO/TCXO Part Number Structure

CH SK - 7 25 L W S□-□□.□□□□□□MHz
 ① ② ⑧+⑨ ⑦ ③ ⑥ Freq.



ex. 725 : 25 x 10⁻⁷ = 2.5ppm

⑦ Output Type(SPXO/TCXO)

Code	Output
H	HCMOS/TTL
S	Sinewave
T	TTL
L	LVCMOS 3.3V
U	Sinewave 3.3V
P	LVPECL 3.3V

Note) 14-pin DIP SPXO/TCXO shall have code COH in place of CHSK(or any package code that follows type code)