

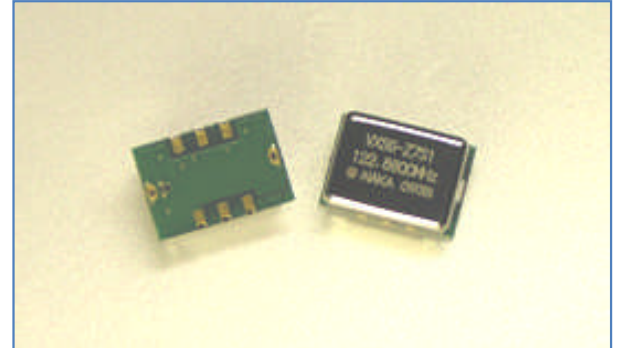
LVPECL VCXO

VXSG-Z7S4

◆ 特長・用途/Feature

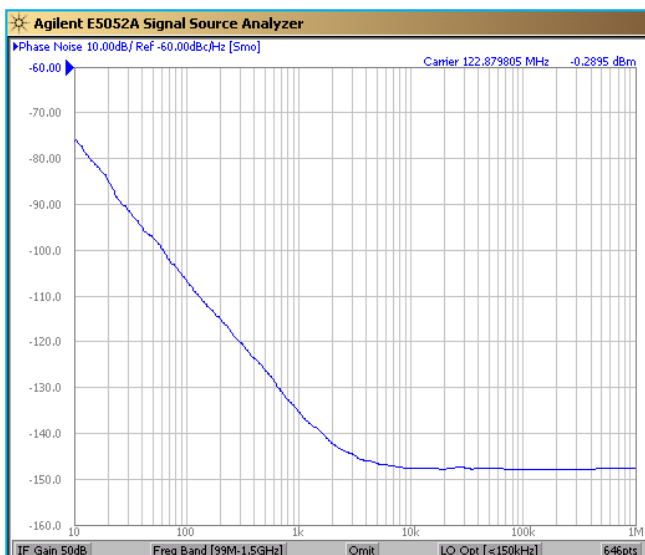
- SMT
- +3.3V LVPECL
- 低位相雑音
Low Phase noise
80 MHz , 120 MHz , 122.88 MHz

◆ 仕様/Specifications

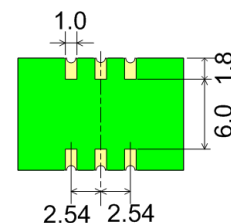
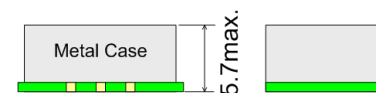
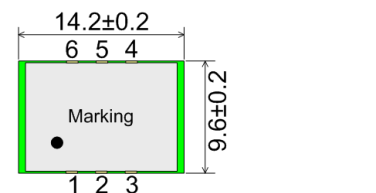


項目 / item	Min	Max	単位 / unit	Note
出力周波数 Frequency range	80	130	MHz	--
動作温度範囲 Operating Temp. Range	-30	+85	°C	--
電源電圧 Power Supply voltage	+3.135	+3.465	V	--
消費電流 Current	--	80	mA	--
周波数安定度 Freq. Overall tolerance	-35	+35	10 ⁻⁶	Inclusive of initial tolerance , operating temperature , supply voltage variation and aging 1 st year.
周波数可変範囲 Freq. control range	±41	--	10 ⁻⁶	制御電圧VC : +0.3V~+3.3V Input impedance : 47kΩ min.
出力レベル Output level LVPECL	VOH	+2.22	--	V
	VOL	--	+1.70	V
Rise time , Fall time	--	2	ns	
Duty Cycle	45	55	%	at +1.96V
位相雑音 Phase noise	--	-100 -130	dBc/Hz	100Hz Offset 1kHz Offset

◆ Phase noise DATA



◆ 外形寸法 / Dimensions unit : mm



Terminal	Connection
1	Vc or NC
2	NC
3	GND
4	Output
5	Output
6	VDD

Pin#1 is NC for SPXO/TCXO